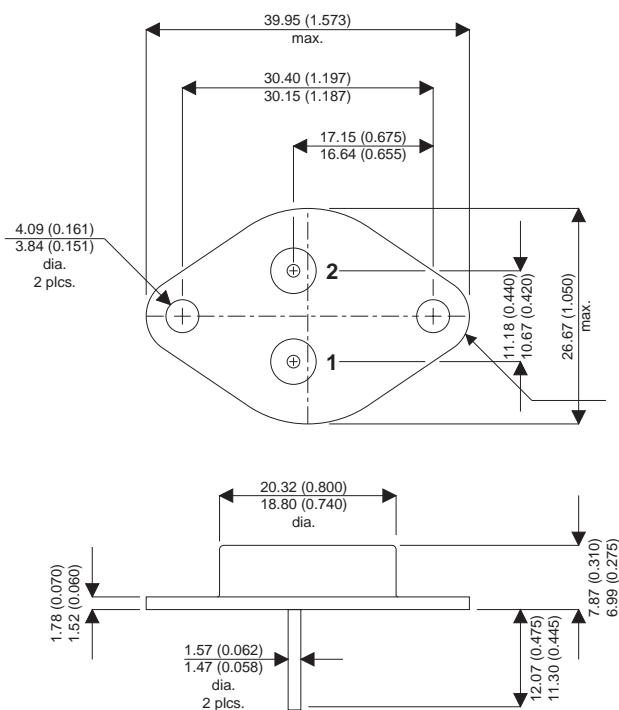


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## MECHANICAL DATA

Dimensions in mm (inches)



**TO-3 Metal Package**

Pin 1 – Gate

Pin 2 – Source

Case – Drain

## N-CHANNEL POWER MOSFET

<b>V<sub>DSS</sub></b>	<b>60V</b>
<b>I<sub>D</sub>(cont)</b>	<b>44A</b>
<b>R<sub>DS(on)</sub></b>	<b>0.028Ω</b>

## FEATURES

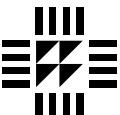
- HERMETICALLY SEALED TO-3 METAL PACKAGE
- SIMPLE DRIVE REQUIREMENTS
- SCREENING OPTIONS AVAILABLE

## ABSOLUTE MAXIMUM RATINGS ( $T_{case} = 25^\circ\text{C}$ unless otherwise stated)

$V_{GS}$	Gate – Source Voltage	$\pm 20\text{V}$
$I_D$	Continuous Drain Current ( $V_{GS} = 0, T_{case} = 25^\circ\text{C}$ )	44A
$I_D$	Continuous Drain Current ( $V_{GS} = 0, T_{case} = 100^\circ\text{C}$ )	27A
$I_{DM}$	Pulsed Drain Current 1	176A
$P_D$	Power Dissipation @ $T_{case} = 25^\circ\text{C}$	125W
	Linear Derating Factor	1.0W/ $^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy 2	340mJ
$dv/dt$	Peak Diode Recovery 3	4.5V/ns
$T_J, T_{stg}$	Operating and Storage Temperature Range	-55 to 150 $^\circ\text{C}$
$T_L$	Lead Temperature 1.6mm (0.63") from case for 10 sec.	300 $^\circ\text{C}$

### Notes

- 1) Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ ,  $\delta \leq 2\%$
- 2) @  $V_{DD} = 25\text{V}$ ,  $L \geq 200\mu\text{H}$ ,  $R_G = 25\Omega$ , Peak  $I_L = 44\text{A}$ , Starting  $T_J = 25^\circ\text{C}$
- 3) @  $I_{SD} \leq 44\text{A}$ ,  $di/dt \leq 250\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ ,  $T_J \leq 150^\circ\text{C}$ , Suggested  $R_G = 9.1\Omega$



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**ELECTRICAL CHARACTERISTICS** ( $T_{case} = 25^\circ C$  unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>STATIC ELECTRICAL RATINGS</b>					
$BV_{DSS}$	Drain – Source Breakdown Voltage $V_{GS} = 0$ $I_D = 1mA$	60			V
$\Delta BV_{DSS}$	Temperature Coefficient of Breakdown Voltage $I_D = 1mA$		0.68		$V/^\circ C$
$R_{DS(on)}$	Static Drain – Source On-State Resistance 1 $V_{GS} = 10V$ $I_D = 27A$			0.028	$\Omega$
	$V_{GS} = 10V$ $I_D = 44A$			0.032	
$V_{GS(th)}$	Gate Threshold Voltage $V_{DS} = V_{GS}$ $I_D = 250mA$	2		4	V
$g_{fs}$	Forward Transconductance <sup>1</sup> $V_{DS} \geq 15V$ $I_{DS} = 27A$	17			S ( $\Omega$ )
$I_{DSS}$	Zero Gate Voltage Drain Current $V_{GS} = 0$ $V_{DS} = 0.8BV_{DSS}$			25	$\mu A$
	$T_J = 125^\circ C$			250	
$I_{GSS}$	Forward Gate – Source Leakage $V_{GS} = 20V$			100	nA
$I_{GSS}$	Reverse Gate – Source Leakage $V_{GS} = -20V$			-100	
<b>DYNAMIC CHARACTERISTICS</b>					
$C_{iss}$	Input Capacitance $V_{GS} = 0$		2400		pF
$C_{oss}$	Output Capacitance $V_{DS} = 25V$		1100		
$C_{rss}$	Reverse Transfer Capacitance $f = 1MHz$		230		
$Q_g$	Total Gate Charge $V_{GS} = 10V$	39		88	nC
$Q_{gs}$	Gate – Source Charge $I_D = 44A$	6.7		15	
$Q_{gd}$	Gate – Drain ("Miller") Charge $V_{DS} = 0.5BV_{DSS}$	18		52	
$t_{d(on)}$	Turn-On Delay Time $V_{DD} = 30V$			23	ns
$t_r$	Rise Time $I_D = 44A$			130	
$t_{d(off)}$	Turn-Off Delay Time $R_G = 9.1\Omega$			81	
$t_f$	Fall Time			79	
<b>SOURCE – DRAIN DIODE CHARACTERISTICS</b>					
$I_S$	Continuous Source Current			44	A
$I_{SM}$	Pulse Source Current <sup>2</sup>			176	
$V_{SD}$	Diode Forward Voltage <sup>1</sup> $I_S = 44A$ $T_J = 25^\circ C$ $V_{GS} = 0$			2.5	V
$t_{rr}$	Reverse Recovery Time $I_F = 44A$ $T_J = 25^\circ C$			220	ns
$Q_{rr}$	Reverse Recovery Charge <sup>1</sup> $d_i / d_t \leq 100A/\mu s$ $V_{DD} \leq 50V$			1.6	$\mu C$
$t_{on}$	Forward Turn-On Time		Negligible		
<b>PACKAGE CHARACTERISTICS</b>					
$L_D$	Internal Drain Inductance (measured from 6mm down drain lead to centre of die)		5.0		nH
$L_S$	Internal Source Inductance (from 6mm down source lead to source bond pad)		13		
<b>THERMAL CHARACTERISTICS</b>					
$R_{\theta JC}$	Thermal Resistance Junction – Case			1.0	$^\circ C/W$
$R_{\theta CS}$	Thermal Resistance Case – Sink		0.12		
$R_{\theta JA}$	Thermal Resistance Junction – Ambient			30	

**Notes**

- 1) Pulse Test: Pulse Width  $\leq 300ms$ ,  $\delta \leq 2\%$
- 2) Repetitive Rating – Pulse width limited by maximum junction temperature.